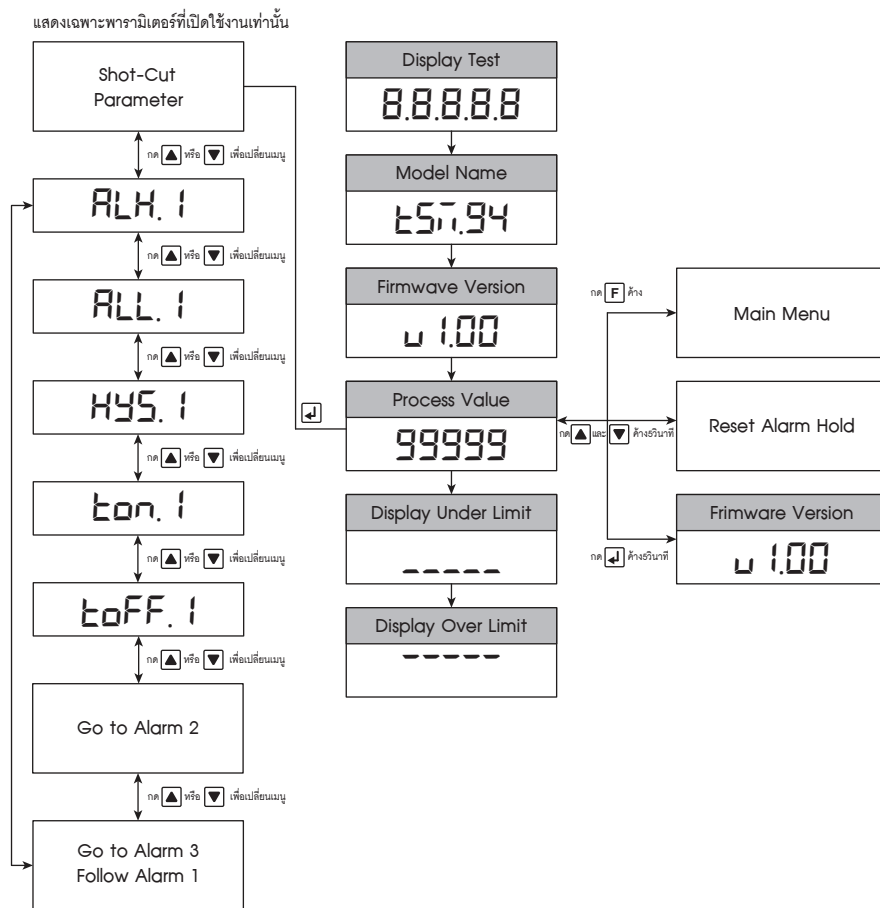
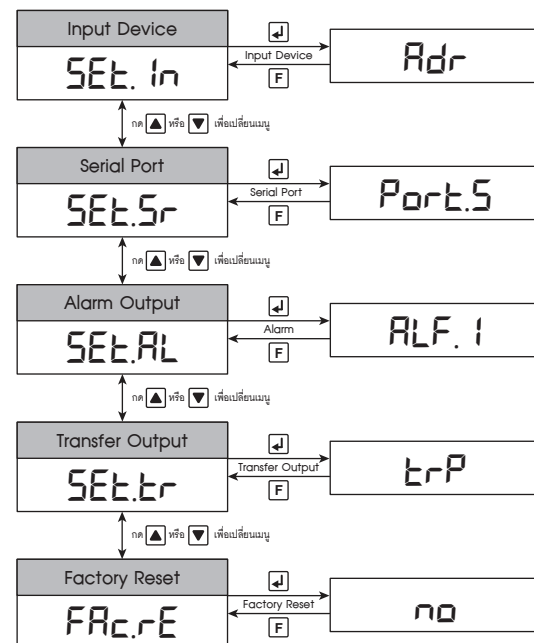


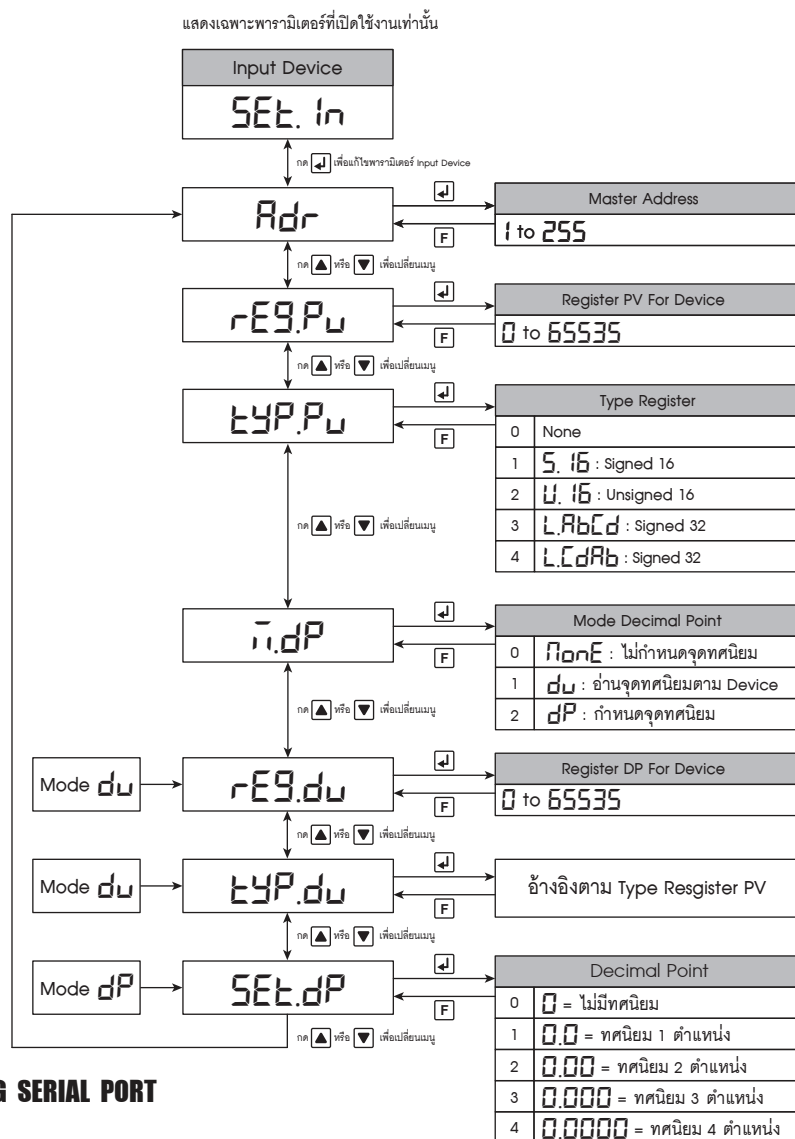
การตั้งค่าพารามิเตอร์



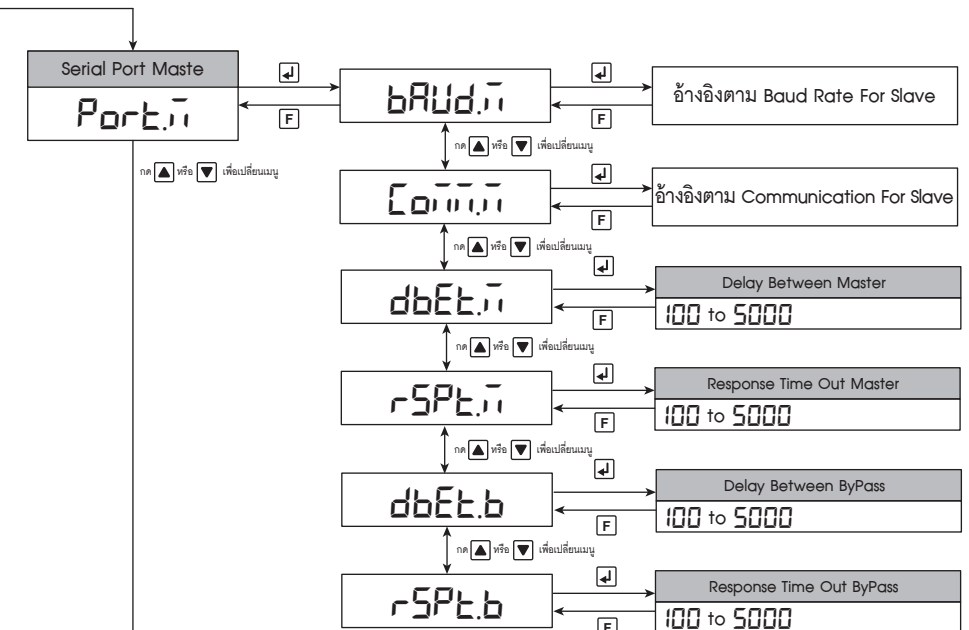
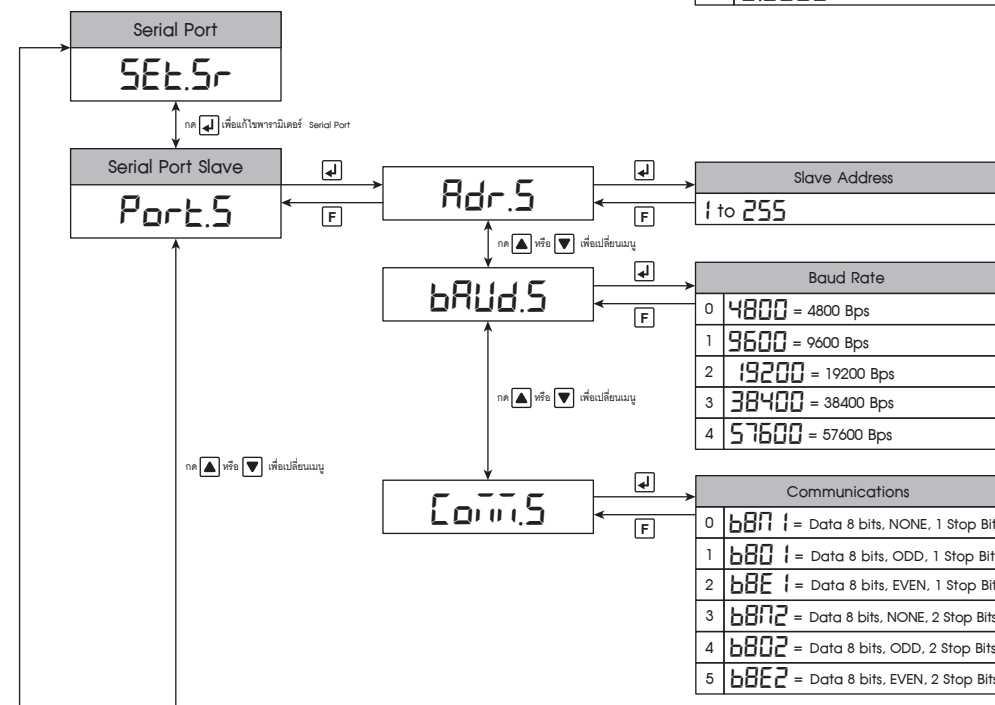
MAIN MENU



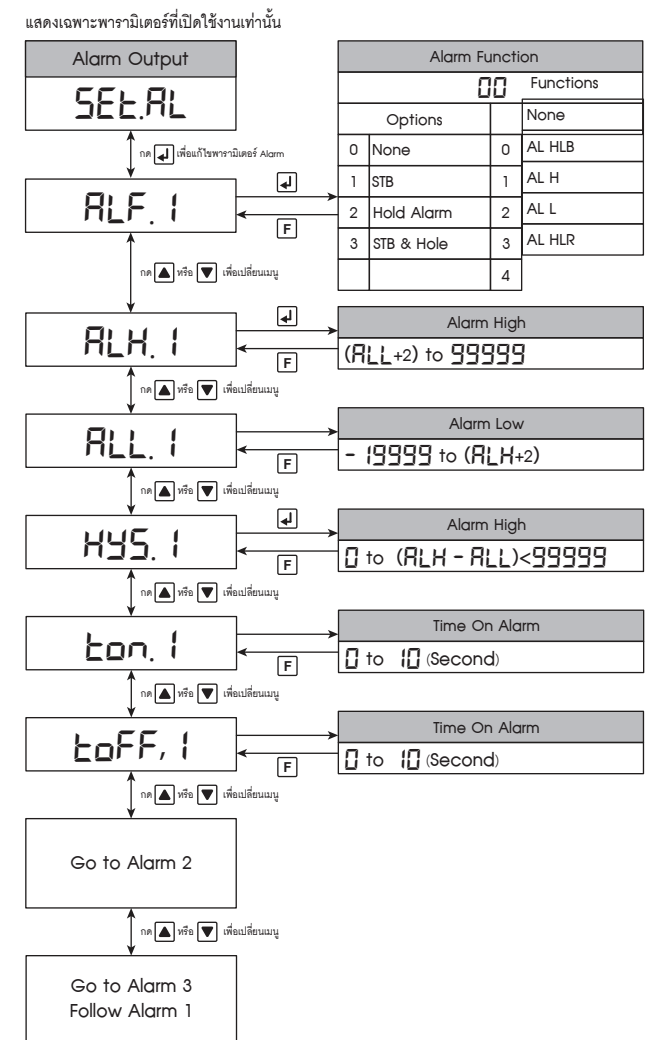
INPUT DEVICE



SETTING SERIAL PORT



SETTING ALARM OUTPUT

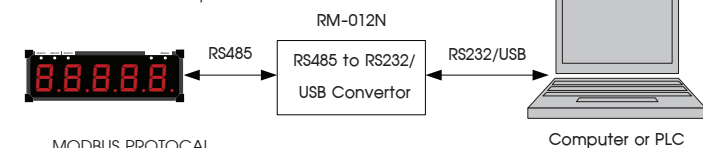


```

graph TD
    A[Transfer Output  
SEt.br] -- "กด [ปุ่ม] เพื่อเพิ่มปริมาณ Transfer Output" --> B[brP]
    B -- "กด [ปุ่ม] หรือ [ปุ่ม] เพื่อเพิ่มเมนู" --> C[brH]
    C -- "กด [ปุ่ม] หรือ [ปุ่ม] เพื่อเพิ่มเมนู" --> D[brL]
    D -- "กด [ปุ่ม] หรือ [ปุ่ม] เพื่อเพิ่มเมนู" --> E[Register PV for Device]
    E --> F[ไม่สลับค่า Transfer Output  
สลับค่า Transfer Output]
  
```

Factory Reset	↕	กด	ไม่ทำการรีเซ็ตค่าเริ่มต้น
FAc.rE	↕	F	ทำการรีเซ็ตค่าเริ่มต้น

The TSM-94B series are equipped with a RS485 serial communications interface to allow connection to computer or PLC. MODBUS protocol is provided as standard communication. The user can connect TSM-94B series as network up to 255 meters.



This MODBUS Protocol has been implement in accordance with MODBUS.ORG MODBUS Application Protocol Specification V1.1 With the following conditions applying

Exception Responses
The following exception codes will be supported only.

- | Address | | Name | Format | Min | Max | Word | Access | Comment |
|----------------|-----|----------------------------|--------|---------|-----------|------|--|--|
| Dec | Hex | | | | | | | |
| 0 | 0 | PV MASTER | Int 32 | -19999 | 99999 | 2 | R | |
| 1 | 1 | | | | | | | |
| 2 | 2 | | | | | | | |
| 3 | 3 | DP MASTER | Int 32 | 0 | 4 | 2 | R | |
| 4 | 4 | | | | | | | |
| Address Master | | Int 16 | 1 | 255 | 1 | R/W | | |
| 5 | 5 | REG PV Master | Int 32 | 0 | 65535 | 2 | R/W | |
| 6 | 6 | | | | | | | |
| 7 | 7 | TYPE PV Master | Int 8 | 0 | 4 | 1 | R/W | 0 : None
1 : S16
2 : U16
3 : ABCD
4 : CDAB |
| 8 | 8 | MODE DP Master | Int 8 | 0 | 2 | 1 | R/W | 0 : None
1 : DV
2 : DP |
| 9 | 9 | REG DP Master | Int 32 | 0 | 65535 | 2 | R/W | |
| 10 | A | | | | | | | |
| 11 | B | TYPE DP Master | Int 8 | 0 | 4 | 1 | R/W | 0 : None
1 : S16
2 : U16
3 : ABCD
4 : CDAB |
| 12 | C | SET DP Master | Int 8 | 0 | 4 | 1 | Mode DP Master < 2 : R
Mode DP Master = 2 : R/W | |
| 13 | D | Address Slave | Int 16 | 1 | 255 | 1 | R/W | |
| 14 | E | BPS Slave | Int 8 | 0 | 4 | 1 | R/W | 0 : 4800bps
1 : 9600bps
2 : 19200bps
3 : 38400bps
4 : 57600bps |
| 15 | F | COMM Slave | Int 8 | 0 | 5 | 1 | R/W | 0 : B8N1 1: B8O1
2 : B8E1 3 : B8N2
4 : B8O2 5 : B8E2 |
| 16 | 10 | BPS Master | Int 8 | 0 | 4 | 1 | R/W | 0 : 4800bps
1 : 9600bps
2 : 19200bps
3 : 38400bps
4 : 57600bps |
| 17 | 11 | COMM Master | Int 8 | 0 | 5 | 1 | R/W | 0 : B8N1 1: B8O1
2 : B8E1 3 : B8N2
4 : B8O2 5 : B8E2 |
| 18 | 12 | Between Master | Int 16 | 100 | 5000 | 1 | R/W | |
| 19 | 13 | RSP Master | Int 16 | 100 | 5000 | 1 | R/W | |
| 20 | 14 | ALF 1 | Int 8 | 0 | 34 | 1 | R/W | Refer to Table Alarm |
| 21 | 15 | ALH 1 | Int 32 | ALL + 2 | 99999 | 2 | R/W | |
| 22 | 16 | | | | | | | |
| 23 | 17 | ALL 1 | Int 32 | -19999 | ALH - 2 | 2 | R/W | |
| 24 | 18 | | | | | | | |
| 25 | 19 | HYS 1 | Int 32 | -19999 | ALH - ALL | 2 | R/W | |
| 26 | 1A | | | | | | | |
| 27 | 1B | Time ON 1 | Int 32 | 0 | 10 | 1 | R/W | |
| 28 | 1C | Time OFF 1 | Int 32 | 0 | 10 | 1 | R/W | |
| 29 | 1D | ALF 2 | Int 8 | 0 | 34 | 1 | R/W | Refer to Table Alarm |
| 30 | 1E | ALH 2 | Int 32 | ALL + 2 | 99999 | 2 | R/W | |
| 31 | 1F | | | | | | | |
| 32 | 20 | ALL 2 | Int 32 | -19999 | ALH - 2 | 2 | R/W | |
| 33 | 21 | | | | | | | |
| 34 | 22 | HYS 2 | Int 32 | -19999 | ALH - ALL | 2 | R/W | |
| 35 | 23 | | | | | | | |
| 36 | 24 | Time ON 2 | Int 32 | 0 | 10 | 1 | R/W | |
| 37 | 25 | Time OFF 2 | Int 32 | 0 | 10 | 1 | R/W | |
| 38 | 26 | ALF 3 | Int 8 | 0 | 34 | 1 | R/W | Refer to Table Alarm |
| 39 | 27 | ALH 3 | Int 32 | ALL + 2 | 99999 | 2 | R/W | |
| 40 | 28 | | | | | | | |
| 41 | 29 | ALL 3 | Int 32 | -19999 | ALH - 2 | 2 | R/W | |
| 42 | 2A | | | | | | | |
| 43 | 2B | HYS 3 | Int 32 | -19999 | ALH - ALL | 2 | R/W | |
| 44 | 2C | | | | | | | |
| 45 | 2D | Time ON 3 | Int 32 | 0 | 10 | 1 | R/W | |
| 46 | 2E | Time OFF 3 | Int 32 | 0 | 10 | 1 | R/W | |
| 47 | 2F | TRP | Int 8 | 0 | 1 | 1 | R/W | 0 : Not Invent
1 : Invent |
| 48 | 30 | TRH | Int 32 | TRL + 1 | ALH - 2 | 2 | R/W | |
| 49 | 31 | | | | | | | |
| 50 | 32 | TRL | Int 32 | -19999 | ALH - ALL | 2 | R/W | |
| 51 | 33 | | | | | | | |
| 52 | 34 | Delay Between ByPass | Int 16 | 100 | 5000 | 1 | R/W | |
| 53 | 35 | Response Time Out ByPass</ | | | | | | |

RW = Read and Write (สามารถอ่านและเขียนได้)

